

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application. Please amend claims 1, 3-11 and 13-20 as follows:

LISTING OF CLAIMS:

1. (Currently Amended) An emulator comprising:
a receiving means for receiving circuit design data used for implementing debugging functions;
a storage means for storing the circuit design data received by said receiving means therein; and
an emulation means for performing emulation according to the circuit design data stored in said storage means.
2. (Original) The emulator according to Claim 1, wherein said receiving means receives a piece of firmware that controls operations of said emulation means.
3. (Currently Amended) The emulator according to Claim 1, further comprising a means for changing a use of a hardware resource by updating the circuit design data stored in said storage means.
4. (Currently Amended) The emulator according to Claim 2, further comprising a means for changing a use of a hardware resource by updating the circuit design data stored in said storage means.

5. (Currently Amended) The emulator according to Claim 1, wherein said receiving means receives the circuit design data from a personal computer.

6. (Currently Amended) The emulator according to Claim 2, wherein said receiving means receives the circuit design data from a personal computer.

7. (Currently Amended) The emulator according to Claim 3, wherein said receiving means receives the circuit design data from a personal computer.

8. (Currently Amended) The emulator according to Claim 1, wherein said receiving means downloads the circuit design data from a homepage.

9. (Currently Amended) The emulator according to Claim 2, wherein said receiving means downloads the circuit design data from a homepage.

10. (Currently Amended) The emulator according to Claim 3, wherein said receiving means downloads the circuit design data from a homepage.

11. (Currently Amended) An emulation method comprising the steps of:
receiving circuit design data used for implementing debugging functions;
storing the circuit design data in a memory; and
performing emulation according to the circuit design data.

12. (Original) The emulation method according to Claim 11, further comprising the step of receiving a piece of firmware that controls the emulation.

13. (Currently Amended) The emulation method according to Claim 11, further comprising the step of changing a use of a hardware resource by updating the circuit design data.

14. (Currently Amended) The emulation method according to Claim 12, further comprising the step of changing a use of a hardware resource by updating the circuit design data.

15. (Currently Amended) The emulation method according to Claim 11, wherein said circuit design data receiving step is the step of receiving the circuit design data from a personal computer.

16. (Currently Amended) The emulation method according to Claim 12, wherein said circuit design data receiving step is the step of receiving the circuit design data from a personal computer.

17. (Currently Amended) The emulation method according to Claim 13, wherein said circuit design data receiving step is the step of receiving the circuit design data from a personal computer.

18. (Currently Amended) The emulation method according to Claim 11, wherein said circuit design data receiving step is the step of downloading the circuit design data from a homepage.

19. (Currently Amended) The emulation method according to Claim 12, wherein said circuit design data receiving step is the step of downloading the circuit design data from a homepage.

20. (Currently Amended) The emulation method according to Claim 13, wherein said circuit design data receiving step is the step of downloading the circuit design data from a homepage.